

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

"Express Mail" mailing label number EK718708045US

Date of Deposit: MAY 25, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date inscribed above and is addressed to the Assistant Commissioner of Patents, Box PATENT APPLICATION, Washington, D.C. 20231.

JOAN PENNINGTON

(Typed or printed name of person mailing paper or fee)

Joan Pennington
(Signature of person mailing paper or fee)

-1-

**TEST METHOD AND APPARATUS FOR PARALLEL OPTICAL
TRANSCIVERS USING SERIAL EQUIPMENT**

Field of the Invention

5 The present invention relates generally to the data processing field, and more particularly, relates to a test method and apparatus for testing parallel optical transceivers using serial equipment.

Description of the Related Art

10 Manufacturing and testing a parallel optical transceiver can be difficult for several reasons. A parallel optical transceiver is made up of parts that do not have yields comparable to circuit boards or complementary metal oxide semiconductor (CMOS) processing with similar integration levels. Optical to electrical conversion at greater than 1 Gbit/s is typically facilitated by GaAs, or other III/V compounds or other exotic materials. For a twelve channel parallel optical transceiver product, the packaging process of 12 lasers, 12
15 optical receivers, 12 laser drivers, and 12 post amplifiers can involve many bonding and gluing operations, and also exotic packaging techniques. Yield is therefore a problem with parallel optical transceivers.

20 Mass producing a module or product that includes a combination of several components that do not yield well individually will cause all of the individual yields to be multiplied together, resulting in an even lower yield for the combined part.

In a production line, when the combined module is built and adjusted

ROC920000150US1

to a point where the module should be functional, it is desirable to test the module to determine that the module is functional. Such preliminary testing should identify if the manufacturing process can continue, or if the module should be scrapped, reworked or sent to another manufacturing line for testing and readjusting channels individually. It is desirable to provide such testing quickly so that manufacturing time could be saved. However, testing each of the parallel channels typically is very time consuming and requires complex, cumbersome and costly test equipment.

When manufacture of a parallel optical transceiver is completed, the problem of quickly testing the parallel optical transceiver in the field is the same as during manufacture. To test the parallel optical transceiver, each of the parallel channels typically must be separately done using the same type of complex, cumbersome and costly test equipment used during manufacture.

A need exists for an improved test method and apparatus for testing parallel optical transceivers.

Summary of the Invention

A principal object of the present invention is to provide a test method and apparatus for testing parallel optical transceivers. Other important objects of the present invention are to provide such test method and apparatus for testing parallel optical transceivers substantially without negative effect and that overcome many of the disadvantages of prior art arrangements.

In brief, a test method and apparatus are provided for testing parallel optical transceivers. Each of a plurality of channels of the parallel optical transceiver is connected in series. A predefined data pattern is applied to a first channel of the series connected plurality of channels. An output is detected from a last channel of the series connected plurality of channels and compared the applied predefined data pattern to identify operation of the parallel optical transceiver.

In accordance with features of the invention, an optical wrap plug and an electrical wrap plug are used for connecting in series the plurality of channels of the parallel optical transceiver. The optical wrap plug includes a plurality of optical connectors for respectively optically connecting each
5 respective channel transmitter to a next respective channel receiver. The electrical wrap plug includes a plurality of electrical connectors for respectively electrically connecting a respective channel receiver to a corresponding respective channel transmitter.

Brief Description of the Drawings

10 The present invention together with the above and other objects and advantages may best be understood from the following detailed description of the preferred embodiments of the invention illustrated in the drawings, wherein:

15 FIG. 1 is a block diagram representation illustrating a test system for testing a parallel optical transceiver in accordance with the preferred embodiment;

FIG. 2 is a block diagram representation illustrating an alternative arrangement for testing multiple parallel optical transceivers in accordance with the preferred embodiment; and

20 FIG. 3 is a block diagram representation illustrating a further alternative arrangement for testing multiple parallel optical transceivers in accordance with the preferred embodiment.

Detailed Description of the Preferred Embodiments

25 Having reference now to the drawings, in FIG. 1, there is shown a test system of the preferred embodiment generally designated by the reference character 100. Test system 100 of the preferred embodiment is used for testing a parallel optical transceiver generally designated by the reference character 102. The parallel optical transceiver 102 includes a plurality of parallel receiver and transmitter channels 1-N, each labeled RX, TX.

In accordance with features of the preferred embodiment, test system 100 enables effectively and efficiently testing of the parallel optical transceiver 102 including each of the plurality of parallel channels RX, TX 1-N using serial equipment. A predefined data pattern is applied to the parallel optical transceiver 102. The predefined data pattern passes through the plurality of parallel channels RX, TX 1-N, with each of the parallel channels connected in series. A serial data detector detects an output of the parallel optical transceiver 102 and compares the detected data output with the applied predefined data pattern to determine whether the parallel optical transceiver is functional. If the output data pattern matches the applied predefined data pattern, then the parallel optical transceiver 102 is functional with all of the plurality of parallel channels RX, TX 1-N functioning properly. If the output data pattern does not match the applied predefined data pattern, then the parallel optical transceiver 102 is not functioning properly with possibly one or more of the plurality of parallel channels RX, TX 1-N having failed.

As shown in FIG. 1, test system 100 includes a serial data generator 104, an optical wrap plug 106, and a serial data detector 108. The serial data generator 104 provides a serial data test signal or predefined data pattern that is applied to the optical wrap plug 106. Optical wrap plug 106 provides optical connections generally designated by 102O between each of the plurality of parallel channels RX, TX 1-N, with each of the parallel channels connected in series. As shown, the optical wrap plug 106 connects the serial data generator 104 to RX1, respectively connects TX1 to RX2; TX2 to RX3, TX3 to RXN, and TXN to the serial data detector 108. A serial data path is provided between a first transceiver optical input RX1 to a last transceiver optical output TXN. An electrical wrap plug 110 provides respective electrical connections generally designated by 102E between the parallel channels RX, TX 1-N. Respective electrical connections 102E are provided between each of the plurality of parallel channels RX, TX 1-N. Each of the parallel channels RX, TX 1-N is connected in series with the electrical wrap plug 110 respectively connecting RX1 to TX1, RX2 to TX2, RX3 to TX3, and RXN to TXN, as shown.

In operation, the serial data generator 104 applies a predefined data pattern to the parallel optical transceiver 102 via the optical wrap plug 106.

ROC920000150US1

The optical wrap plug 106 couples the predefined data pattern from the serial data generator 104 to RX1. The electrical wrap connector 110 connects the electrical outputs of the respective parallel channel receiver RX to the corresponding parallel channel transmitter TX. The optical wrap plug
5 106 connects the optical outputs of the respective parallel channel transmitter TX to a next parallel channel receiver RX. The predefined data pattern applied to first parallel channel receiver RX1 goes through all the series connected parallel channels RX, TX 1-N and is coupled from the last parallel channel transmitter TXN in the parallel optical transceiver 102 to the
10 serial data detector 108 via the optical wrap plug 106.

The serial data detector 108 detects the output of the parallel optical transceiver 102 and compares the detected data output with the applied predefined data pattern to determine whether the parallel optical transceiver 102 is functional. When the parallel optical transceiver 102 is operating
15 correctly, then the serial data detector 108 detects that the output data pattern matches the applied predefined data pattern. When the parallel optical transceiver 102 has failed, then the serial data detector 108 detects that the output data pattern does not match the applied predefined data pattern. While diagnostic information for individual channels is not provided
20 by test system 100, if the data pattern comes out in the same form it went into the parallel optical transceiver 102, then all channels are working. Also if the optical output has a 10^{-12} bit error rate then the bit error rate for each channel would not be greater than the 10^{-12} bit error rate.

Referring now to FIG. 2, there is shown an alternative arrangement
25 for testing multiple parallel optical transceivers in accordance with the preferred embodiment generally designated by the reference character 200. In FIG. 2, the same reference characters are used for identical or similar components as used for the test system 100 of FIG. 1. Testing apparatus 200 enables simultaneously testing multiple parallel optical transceivers 102.
30 Testing apparatus 200 includes a serial data generator 104 and a serial data detector 108. A plurality of test modules 1-M, 202 is connected in series between the serial data generator 104 and the serial data detector 108. Each of the plurality of test modules 1-M, 202 includes a parallel optical transceiver 102 connected together with an optical wrap plug 106, as shown
35 in FIG. 1. An MxN array electrical wrap fixture 210 connects all of the

plurality of test modules 1-M, 202. N-wide electrical connections 212 are provided by the MxN array electrical wrap fixture 210 for connecting all of the plurality of parallel channels RX, TX 1-N as shown in FIG. 1, of each respective parallel optical transceiver 102 of each respective test module 1-M, 202. Each of the channels RX, TX, 1-N of parallel optical transceiver 102 included in each test module 202 is connected together as shown in FIG. 1.

In operation, the serial data generator 104 applies a predefined data pattern to the first test module 1, 202. The predefined data pattern is provided to the next serial test module 2, 202. The predefined data pattern is provided in a serial fashion though each of the modules 1-M, 202. The serial data detector 108 detects the output of the test module M, 202 and compares the detected data output with the applied predefined data pattern to determine whether the test modules 1-M, 202 are functional. When the test modules 1-M, 202 are operating correctly, then the serial data detector 108 detects that the output data pattern matches the applied predefined data pattern. When the test modules 1-M, 202 have failed, then the serial data detector 108 detects that the output data pattern does not match the applied predefined data pattern.

FIG. 3 is a block diagram representation illustrating a further alternative arrangement for testing multiple parallel optical transceivers in accordance with the preferred embodiment. In FIG. 3, the same reference characters are used for identical or similar components as used for the test system 100 of FIG. 1. Testing apparatus 300 enables simultaneously testing multiple parallel optical transceivers 102. Testing apparatus 300 includes a serial data generator 104 and a serial data detector 108. A plurality of test modules 1-M, 302 is connected in series between the serial data generator 104 and the serial data detector 108. Each of the test modules 1-M, 302 includes a parallel optical transceiver 102 connected together with an optical wrap plug 106 and an electrical wrap fixture 110 as shown in FIG. 1. Each of the channels RX, TX, 1-N of parallel optical transceiver 102 included in each of the test modules 1-M, 302 is connected together as shown in FIG. 1.

In operation, the serial data generator 104 applies a predefined data pattern to the first test module 1, 302. The predefined data pattern is provided to the next serial test module 2, 302. The predefined data pattern

is provided in a serial fashion though each of the modules 1-M, 302. The serial data detector 108 detects the output of the test module M, 302 and compares the detected data output with the applied predefined data pattern to determine whether the test modules 1-M, 302 are functional. When the

5 test modules 1-M, 302 are operating correctly, then the serial data detector 108 detects that the output data pattern matches the applied predefined data pattern. When the test modules 1-M, 202 have failed, then the serial data detector 108 detects that the output data pattern does not match the applied predefined data pattern.

10 While the present invention has been described with reference to the details of the embodiments of the invention shown in the drawing, these details are not intended to limit the scope of the invention as claimed in the appended claims.

10986260 09259860
T05230 032301